New Directions in Channelized Receivers and Transmitters

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Motivation For Using Multirate Filters

Processing Task: Obtain Digital Samples of Complex Envelope Residing at Frequency f_C









Replicate Analog Processing in DSP





Fundamental Operations
Select Frequency
Limit Bandwidth
Select Sample Rate



Digital Down Converter (DDC)

Spectral Description of Fundamental Operations



A Shell Game: Rearrange the Players! Keep Your Eye on the Pea!







Down Sample Complex Digital IF





Fundamental Operation with Rearrange Operators

Up-Convert Filter, Filter Signal at IF, Down Convert Output of Filter



Equivalency Theorem

Down-Convert Signal at Input to Low-Pass Filter

$$r(n) = s(n)e^{-j\theta_0 n} * h(k)$$

= $\sum_k s(n-k)e^{-j\theta_0(n-k)}h(k)$
= $e^{-j\theta_0 n}\sum_k s(n-k)h(k)e^{j\theta_0 k}$
= $e^{-j\theta_0 n} \{s(n) * h(n)e^{j\theta_0 n}\}$

Down-Convert Signal at Output of Band-Pass Filter Band-Pass Filter



Not Finished: Moving Down Converter from Input to Output Replaces 2-Multipliers (Complex Scalar) with 4-Multipliers (Complex Product)

y(n)

y(n)

Interchange Down Converter and Resampler



Only Down Convert the Samples we Intend to Keep! Let the Resampler Alias the Center Frequency to Baseband



Successive Transformations Turn Sampled Data Version of Edwin Armstrong's Heterodyne Receiver to Tuned Radio Frequency (TRF) Receiver and then to Aliased TRF Receiver.



Let's Keep Rearranging the Players!



Linear Systems Commute and are Associative



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In Case you Couldn't Wait to See the Proof

 $y(n) = \sum x(n-k_1)h(k_1)$

$$dy(n) = \sum_{k_2} y(n-k_2)g(k_2)$$

$$= \sum_{k_2} \sum_{k_1} x(n-k_1-k_2)h(k_1)g(k_2)$$

$$= \sum_{k_2} \sum_{k_3} x(n-k_3)h(k_3-k_2)g(k_2)$$

$$= \sum_{k_3} x(n-k_3)\sum_{k_2} h(k_3-k_2)g(k_2)$$

$$= \sum_{k_3} x(n-k_3)f(k_3)$$
where $f(n) = \sum_{k_2} h(n-k_2)g(k_2)$

$$x(n-k_{3})h(k_{3}-k_{2})g(k_{2})$$

$$(k-k_{3})\sum_{k_{2}}h(k_{3}-k_{2})g(k_{2})$$

$$(k-k_{3})f(k_{3})$$

$$(n) = \sum_{k_{2}}h(n-k_{2})g(k_{2})$$



Filter and Output Resampler can Commute to Input Resampler and Resampled Filter







Coefficient Assignment of Low-Pass Polyphase Partition

For M-to-I resample start at Index r and Increment by M For 3-to-I resample start at index r and increment by 3



Extract Delays To First Non-Zero Coefficient

This mapping from I-D to 2-D is used by Cooley-Tukey FFT. Polyphase Filters and CT-FFT are kissing cousins!

Polyphase Partition of Low Pass Filter I-Path to M-Path Transformation



M-Path Partition Supports M-to-I Down Sample Also Supports Rational Ratio M-to-Q and M-to-Q/P Down Sample!



M-Units of Delay at Input Rate Same as I-Unit of Delay at Output Rate



Interchange Filters and Resampler: Place Resampler at Input Rather Than at Output of Filter



Replace Delays with Commutator Perform Path Operations Sequentially



Transmitter Process, Up-Sample and Up-Convert **Receiver Process, Down Convert and Down-Sample** Modulator Raises Sample Rate & Applies Heterodyne at High Output Sample Rate! Modulator Clock DIGITAL Analog Analog I-Q Bits Interpolate Table STA RF Camer cos(θ_n) $sin(\theta_n)$ Shape Upsample Channel Channel Demodulator TIMING Clock Gain Control Carrier NGI Waveform Analog Bits Decimate Detect P-S Oscillator RF Carrier sin $cos(\theta_0 n)$ Matched Filter

De-Modulator Applies Heterodynes

Conventional and Ubiquitous DDC



Convert Two Parallel Paths into M Sequential Paths for each Path



Replace CIC with Cascade 2-to-1 Half Band FIR Filters



Filter Number	1	2	3	4	5	6	7	8	9	10	Total
Number Taps	3	3	3	3	7	7	7	7	11	19	70
Operations	2-A	2-A	2-A	2-A	4-A	4-A	4-A	4-A	6-A	10-A	
Per Filter	2-Shifts	2-Shifts	2-Shifts	2-Shifts	2-Mult	2-Mult	2-Mult	2-Mult	3-Mult	5-Mult	
Adds Ref to Input	2	2/2	2/4	2/8	4/16	4/32	4/64	4/128	6/256	10/512	4.26
Mult Ref to Input	0	0	0	0	2/16	2/32	2/64	2/128	3/256	5/512	0.27

Impulse and Frequency Response of Last Stage Referred to Earlier Stages



Replace CIC with Cascade 2-to-1 Half Band Linear Phase IIR Filters



Filter Number	1	2	3	4	5	6	7	8	9	10	Total
Number Taps	1	1	1	1	3	3	3	3	3	4	23
Operations Per Filter	3-A 1-Mult	3-A 1-Mult	3-A 1-Mult	3-A 1-Mult	7-A 3-Mult	7-A 3-Mult	7-A 3-Mult	7-A 3-Mult	7-A 3-Mult	9-A 4-Mult	
Adds Ref to	3/2	3/4	3/8	3/16	7/32	7/64	7/128	7/256	7/512	9/1024	3.25
Mult Ref to Input	1/2	1/4	1/8	1/16	3/32	3/64	3/128	3/256	3/512	4/1024	1.12

Impulse and Frequency Response of Last Stage Referred to Earlier Stages




2-to-I Resampling 2-Path Polyphase Filter and Digital Down-Converter



- I Resampling 4-Path Down-Sample Polyphase Filter and 4-Point IFFT Extracts Signal Component From One-of-Four Selected Nyquist Zones





2-to-1 00wn-Sample 4-Path Polyphase FilterPath-0 Not Used2.5-Multiplies per Input

4-Phase Rotators $fs \cdot k/4$: { $C_0 C_1 C_2 C_3$ } $fs \cdot 0/4$: {1 1 1 1} $fs \cdot 1/4$: {1 j -1 -j} $fs \cdot 2/4$: {1 -1 1 -1} $fs \cdot 3/4$: {1 -i -1 i}

Four Bands Centered on the Cardinal Directions



Bands Centered on 0° and 180 ° (DC and f_s/2) Alias To DC When Down-Sampled 2-to-1

Bands Centered on +90° and -90 ° (+f_S/4 and –f_S/4) Alias To fs/2 When Down-Sampled 2-to-1

Spectra: Four Half Band Filters on Unit Circle Showing Alias Free Pass, Transition, and Aliased Bands



Any Narrowband Signal Must Reside in One of the 4 Alias Free Band Intervals. The Alias Free Band Intervals Overlap!



Frequency Responses of Four Nyquist Zone Filters







Most Efficient Multistage Half-Band Digital Down-Converter





Spectrum of Input Signal and Zoom to Spectral Segment



Spectra: Last Four Stages Processing Chain. Dotted Line Indicates Center Frequency of Desired Spectral Component



ampled Data Frequency Locations on Successive Aliases





A 375-to-1 down-sample:

90 MHz to 240 kHz with a 30 kHz output BW 80 dB dynamic range.

Require 6 CIC stages. The gain of each stage is 375:

Gain of 6 stages becomes (375)⁶ or 2.8 ·10¹⁵ or

52 bits growth in the CIC integrators.

With 16-bit input data integrator bit width is 16+52 or 68. Six integrators in both I & Q paths would be circulating 816 bits per input sample which if converted to the 16-bit width required of the arithmetic in the half-band filters proves to be same number of bits to manipulate 48 arithmetic operations per input sample.

Number of operations for the I-Q half band filter chain is on the order of 8-multiply and 16 adds per input sample which represents a workload 1/6 of the CIC chain. The efficient cascade CIC filter chain can be replaced with an even more efficient cascade four-path half band filter chain.

Linear Phase IIR Filter



Most Efficient Multistage Half-Band Digital Down-Converter I-Q I-Q RL I-Q hannelizer nannelizei hannelize nannelizei 4-Path 4-Path 4-Path 4-Path 2-to-1 2-to-1 2-to-1 2-to-1 Filte Down Down Down Down Sample Sample Sample Sample Channel DDS Select

mpulse Response, Two-Path, 4-Coefficient, Linear Phase IIR



Polyphase Partition of Band Pass Filter I-Path to M-Path Transformation Modulation Theorem of Z-Transform $G(Z) = \sum^{N-1} h(n) e^{j\theta_k n} Z^{-n} = \sum^{N-1} h(n) (e^{-j\theta_k} Z)^{-n} = H(e^{-j\theta_k} Z)$ M - 1 N - 1 $G(Z) = \sum \sum h(r + nM) e^{j\theta_k(r + nM)} Z^{-(r + nM)}$ $r=0 \ n=0$ $G(Z) = \sum_{k=1}^{M-1} e^{j\theta_{k}} Z^{-r} \sum_{k=1}^{N-1} h(r + nM) e^{j\theta_{k}nM} Z^{-nM}$ $\theta_{k} = \mathbf{k} \cdot 2\pi$ $\theta_{k} = k \cdot \frac{2\pi}{M} \qquad G(Z) = \sum^{M-1} e^{j\frac{2\pi}{M}k} Z^{-r} \sum^{N-1} h(r + nM) Z^{-nM}$



Apply Noble Identity to Polyphase Partition



Move Phase Spinners to Output of Polyphase Filter Paths



Polyphase Partition with Commutator Replacing the "r" Delays in the "r-th" Path



x(n)

Note:We don't assign Phase Spinners to Select Desired Center Frequency Till after Down Sampling And Path Processing

This Means that The Processing for every Channel is the same till the Phase Spinner

No longer LTI, Filter now has M-Different Impulse Responses! Now LTV or PTV Filter.

Armstrong to Tuned RF with Alias Down Conversion to Polyphase Receiver



Rather than selecting center frequency at input and reduce sample rate at output, we reverse the order, reduce sample rate at input and select center frequency at output. We perform arithmetic operations at low output rate rather than at high input rate!



Polyphase Partition I-D filter becomes 2-D M-Path Filter





Phase and Gain Response

(3-Versions of Filter)

Prototype Filter,

Polyphase Filter Prior to Resampling,

Polyphase Filter after Resampling

Impulse Response and Frequency Response of Prototype Low Pass FIR Filter



Impulse Response of 6-Path Polyphase Partition Prior to 6-to-1 Resampling





Frequency Response of 6-Path Polyphase Partition Prior to 6-to-1 Resampling





Phase Response of 6-Path Polyphase Partition Prior to 6-to-1 Resampling



Overlay Phase Response of 6-Path Polyphase Partition Prior to 6-to-1 Resampling



De-Trended Overlay Phase Response: 6-Path Partition Prior to 6-to-1 Resampling





Overlay 3-D Paddle-Wheel Phase Profiles, 6-Path Partition Prior to 6-to-1 Resampling



Overlay 3-D Paddle-Wheel Phase Profiles, Showing Phase Shifts in +1 Nyquist Zone



Overlay 3-D Paddle-Wheel Phase Profiles, Phase Shifted to Align Phases in +1 Nyquist Zone




PolyChanDemo

Single Channel Armstrong and Multirate Aliased Polyphase Receiver



de Input Down Conversion to Output of Filter Where it anishes Due to Down Sampling. Rotators in Filter Factor Out and are Applied to Path Outputs Rather than to Coefficients.

Advantage: Real sequence is made complex at output of Filter Rather than at Input to Filter



Bad Mismatch: Sample Rate Large Compared to Transition Bandwidth



Polyphase Partition of Low-Pass Filter



Cascade Polyphase Filter Down-Sampling and Up-Sampling



Efficient Polyphase Filter Implementation







Polyphase Partition of Low-Pass Filter



Polyphase Partition of Band Pass Filter



lyphase Partition of Two Band Pass Filters



Vorkload for Multiple M-Path Filters

- I-Channel M-to-I Down Sample
 - I-Filter and M Complex Phase Rotators
- 2-Channels M-to-I Down Sample
 - I-Filter and 2M Complex Phase Rotators
 - K-Channels M-to-I Down Sample
 - I-Filter and kM Complex Phase Rotators
- M-channels M-to-I Down Sample (use FFT)
 - I-Filter and [log₂(M)/2]M Complex Phase Rotators

en k > Log₂(M)/2 Build all channels and discard the channels you don't need! M=16, Log₂(16)/2 = 2: thus if you want 2 or more, Build them all! M=128, Log₂(128)/2 = 3.5: thus if you want 4 or more, Build them all! M=1024, Log₂(1024)/2 = 5: thus if you want 5 or more. Build them all! M-Channel Channelizer: Resampled M-Path Narrowband Filter Channels Alias to Baseband: Phase Aligned Sums Separate Aliases: rk Performed at Low Output Rate Rather Than at High Input Rate. One Input Filter Services M-Output Channels



Dual Channel Armstrong and Multirate Aliased Polyphase Receiver



Up-sampling by Zero Packing and Filtering

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Spectra Of Input, of Zero-Packed, and of Low-Pass Filtered Zero-Packed Signal



Spectra Of Input, of Zero-Packed, and of Band Pass Filtered Zero-Pack Signal







Noble Identity: Interchange M-Delays with M-to-1 Resample

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Interchange Filter and Resampler







Low-Pass Replaced by Band-Pass

$$G(Z) = \sum_{n=0}^{N-1} h(n) e^{j\frac{2\pi}{M}kn} Z^{-n}$$

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$$G(Z) = \sum_{r=0}^{M-1} \sum_{n=0}^{M-1} h(r+nM) e^{j\frac{2\pi}{M}(r+nM)k} Z^{-(r+nM)}$$

$$G(Z) = \sum_{r=0}^{M-1} Z^{-r} e^{j\frac{2\pi}{M}rk} \sum_{n=0}^{M-1} h(r+nM) e^{j\frac{2\pi}{M}nM} Z^{-nM}$$
$$G(Z) = \sum_{r=0}^{M-1} Z^{-r} e^{j\frac{2\pi}{M}rk} \sum_{n=0}^{M-1} h(r+nM) Z^{-nM}$$

Spin The Delays, Don't Touch the M-Path Partitioned Weights



M-Path, M-Channel Channelizer: Spinners are in IFFT

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M-Point IFFT Supplies Phase Spinners to Form Up Converters to all Multiples of Input Sample Rate



All Output Channels Centered on Multiples of Input Sample Rate

Example: Multiples of 6-MHz

Heterodyne Input Signal a Small Frequency Offset from DC: Channelizer Aliases DC to Channel Center and offset signal from DC is Offset from Channel Center













Various Filter-Channelizer Configurations





Prototype Low-Pass Filter for 120 Channel Channelizer



M-Path Polyphase Filter and M/2-to-1 Down Sampling



Use Noble Identity to Pull M/2-to-1 Resampler Through Path Filter

Path Filters: Polynomials in Z^M Converted to Polynomials in Z²



Use Noble Identity to Pull M/2-to-1 Resampler Through Delays in Lower Half of Paths



Replace Delays and M/2-to-1 Resamplers with Dual Input M/2 Path Commutator


Fold Unit Delays in lower half Filter Paths Into Filter Polynomials in Z²



M-to-1 Down Sample Aliases Multiples of Output Sample Rate to DC M/2-to-1 Down Sample Aliases Odd Multiples of Output Sample Rate to Half sample Rate



Circular Buffer Between Polyphase Filter and IFFT Aligns Shifting Input Origin with IFFT's Origin



M/2-to-1 Analysis Channelizer



1-to-M/2 Synthesis Channelizer



Frequency Domain Filtering With Cascade M/2-to-1 Analysis and 1-to-M/2 Synthesis Channelizers





Impulse Response and Frequency Response 40-Enabled Ports: 3.9 MHz Bandwidth



Mixed, Arbitrary Bandwidth Channelizers



Mixed Bandwidth Signals presented to Channel Synthesizer



ompose Broadband Signals Using Short Analysis rs and Present Components to Synthesizer



0-MHz Input Signal Partitioned into ive 10-MHz Sub Channels: f_s=20 MHz



Iultiple Partitioned Input Bands resented to Synthesizer



Assembled Multiple BW Channels in Single Synthesis Channelizer



Reassemble Decomposed Broadband Signals Using Short Synthesis Filters formed by Multiple Channel Analysis Channelizer



artitioned spectral Components from ingle Multi-Channel Analyzer



eassembled Wide band Channels om Short Synthesis Channelizers



Signal Fidelity Preserved under Multiple Sub-Channel Disassembly and Reassembly





Cascade M/2-to-1 Analysis and 1-to-M/2 Synthesis Channelizers Frequency Domain Filtering and Spectral Shuffle







Suspicions Confirmed!



Dilbert, is it true that DSP makes the world go around but multirate signal processing supplies the music for the ride?



Is There any Doubt???

